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2815

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/801,311	
	Filing Date	March 13, 2001	
	First Named Inventor	Yasuhiro SHIMADA et al.	
	Group Art Unit	2815	
	Examiner Name	E. J. Wojchecchowicz	
Total Number of Pages in This Submission		Attorney Docket Number	740819-520

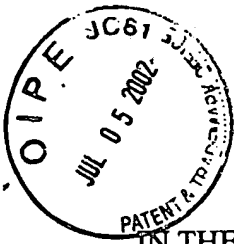
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Firm or Individual name	<u>Donald R. Studebaker, Reg. No. 32,815</u> Nixon Peabody LLP 8180 Greensboro Drive Suite 800 McLean, VA 22102
Signature	
Date	July 1, 2002

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Docket No. 740819-520

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Yasuhiro SHIMADA et al.
Application Serial No. 09/804,311
Filed: March 13, 2001
For: SEMICONDUCTOR DEVICE AND
METHOD FOR FABRICATING THE
SAME

) Group Art Unit: 2815

) Examiner: E. J. Wojchechowski

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) Gwyn J. Cantrell
) Gwyn J. Cantrell

AMENDMENT

Honorable Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

In response to the Examiner's non-final Office Action mailed April 1, 2002, please consider the following amendments and remarks in connection with the above-identified application.

IN THE CLAIMS:

Please cancel claim 5 in its entirety without prejudice or disclaimer to the subject matter recited therein.

Please amend claims 1, 6 and 7 as follows. Claims 1, 6 and 7 are presented below in their amended form. The amendments to the above-noted claims are outlined in an Attachment to the Amendment using the conventional indication method of bracketing and underlining.

1. (Amended) A semiconductor device comprising:
a semiconductor substrate;
an MISFET, which is provided on the semiconductor substrate and includes a gate insulating film, a gate electrode and source/drain regions;